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Class	Subclass
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PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. TR PD3 AA SCANNED Q.A.	PATENT DATE
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APPLICATION NO. 09/726114	CONT/PRIOR F	CLASS 251 ⁷⁰³ 412	SUBCLASS 13 ¹⁵	ART UNIT ²¹²³ 2811 2826	EXAMINER 2121 CRAIG
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Shigetaka Kumashiro

Apparatus and method for simulating MOSFET

PTO-2040
12/99

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION									

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)		ISSUE FEE	
Amount Due			Date Paid	
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